

AMENDMENT

In the Specification:

On page 11, please delete the first paragraph and replace it with:

Figure 4 shows an exemplar fuse sense circuit 400 with a matched gain stage. The fuse sense circuit 400 has a post amplifier device 426 with its source tied to the voltage and a post amplifier load 422 with its gate tied to the voltage 232 instead of a sense enable signal input node 560. This embodiment allows the post amplifier load 422 to pull down a post amplifier output node 450 when the sense amplifier 402 is powered down (*i.e.*, when the sense enable signal is de-asserted).

In The Claims:

Please cancel claims 10-20 without prejudice or disclaimer.

Please amend claims 1-7 as follows.

1. (Amended) An apparatus, comprising:
 - first circuit branch;
 - second circuit branch, coupled to the first circuit branch in a current mirror configuration;
 - an amplifier, coupled to the second circuit branch, comprising a scaled replica of the first branch and an amplifier output node; and
 - an enable node coupled to the first circuit branch, the second circuit branch, and the amplifier to indicate a first logical state at the amplifier output node if the first circuit branch is programmed and the second circuit branch is un-programmed and to indicate a second logical state at the amplifier output node if the first circuit branch is un-programmed and the second branch is programmed.